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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Re
1			US 6163836 A	20001219	22	Processor with programmable addressing modes	712/37	712/209; 712/32;	
2			US 5987603 A	19991116	12	Apparatus and method for reversing bits using a shifter	712/300	708/209; 712/223	
3			US 5926644 A	19990720	28	Instruction formats/instruction encoding	712/22	712/24; 717/149	
4			US 4931974 A	19900605	12	Sixteen-bit programmable pipelined arithmetic logic unit	708/521	708/508	
5			US 4075704 A	19780221	19	Floating point data processor for high speed operation	708/507	708/503; 708/505;	
6			US 5682340 A	19971028	13	Low power consumption circuit and method of operation for implementing shifts and bit reversals	708/209	377/54; 377/69;	
7			US 3768077 A	19731023	7	DATA PROCESSOR WITH REFLECT CAPABILITY FOR SHIFT OPERATIONS	708/200	708/625; 708/653;	